

	Class	Subclass
ISSUE CLASSIFICATION		

PATENT NUMBER


U.S. **UTILITY** Patent Application

**O.I.P.E**

PATENT DATE

**SCANNED**

**Q.A**

APPLICATION NO. 09/893577	CONT/PRIOR F	CLASS 438	SUBCLASS 719	ART UNIT 1765	EXAMINER  Chen, K
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## APPLICANTS

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# Fabrication method of semiconductor integrated circuit device

**TITLE**

PTO-2040  
12/99**ISSUING CLASSIFICATION**

ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

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<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b> Sheets Drwg.      Figs. Drwg.      Print Fig.		<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)      _____ (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____	_____ (Primary Examiner)      _____ (Date)		<b>ISSUE FEE</b>	
			Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)      _____ (Date)		<b>ISSUE BATCH NUMBER</b>	

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